

**Amendments to the Drawings:**

The attached sheet of drawings includes changes to Figures 1A-1D. This sheet, which includes Figs 1A-1D, replaces the original sheet including Figure 1A-1D. In Figures 1A-1D, the legend "Prior Art" has been added.

Attachment: Replacement Sheet



Attorney's Docket No.: 42390.P7162

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Moas, et al.

Application No. 09/458,121

Filed: 12/08/1999

For: METHOD AND APPARATUS  
FOR STACK EMULATION DURING  
BINARY TRANSLATION

Examiner: Matthew P. Gubiot

**RECEIVED**

AUG 11 2003

Art Unit: 2124

Technology Center 2100

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#10  
S. Cotton  
8-13-03

Commissioner For Patents  
Alexandria VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Sir:

Applicants respectfully submit 3 sheets of drawings as follows: Figs. 1A, 1B, 1C, 1D, 2, and 3.

If the Examiner believes a telephone conference would expedite or assist in the allowance of the present application, the Examiner is invited to call John Ward at (408) 720-8300.

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: 8/4/03

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INITIAL STATE

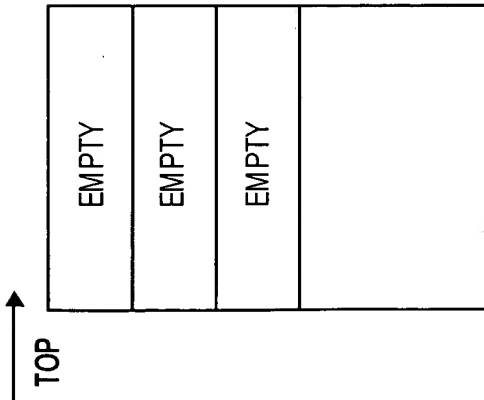


FIG. 1A  
(PRIOR ART)

PUSH FP VALUE ONTO STACK

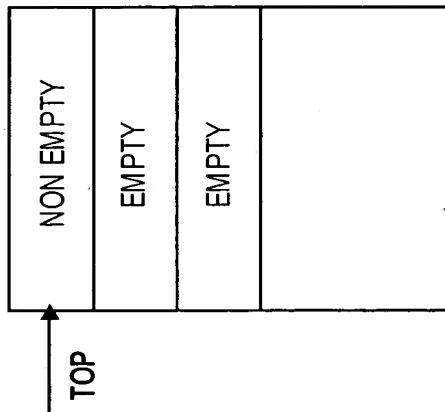


FIG. 1B  
(PRIOR ART)

POP FP VALUE OFF STACK:  
CAUSE NO STACK FAULT

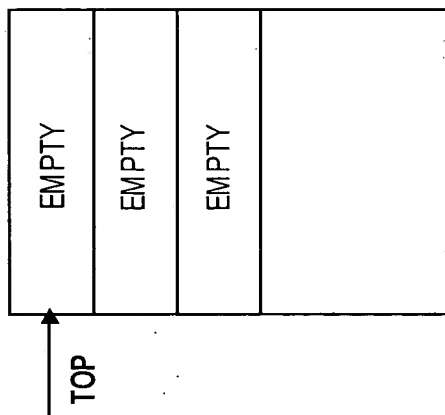


FIG. 1C  
(PRIOR ART)

POP FP VALUE OFF STACK:  
CAUSE A STACK UNDERFLOW

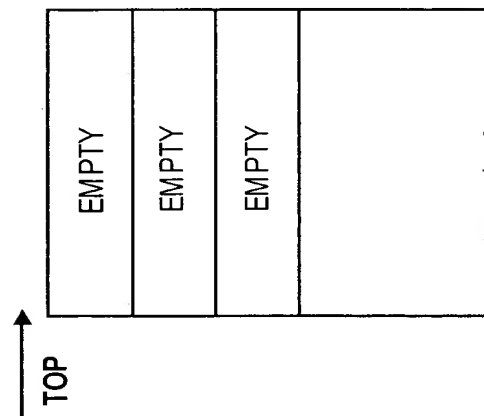


FIG. 1D  
(PRIOR ART)

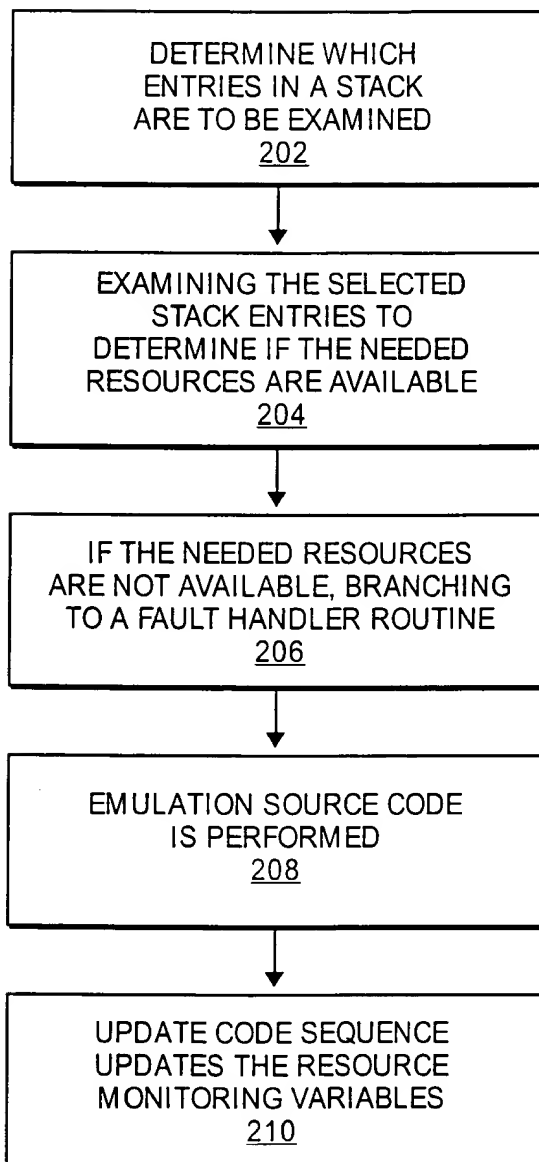


FIG. 2

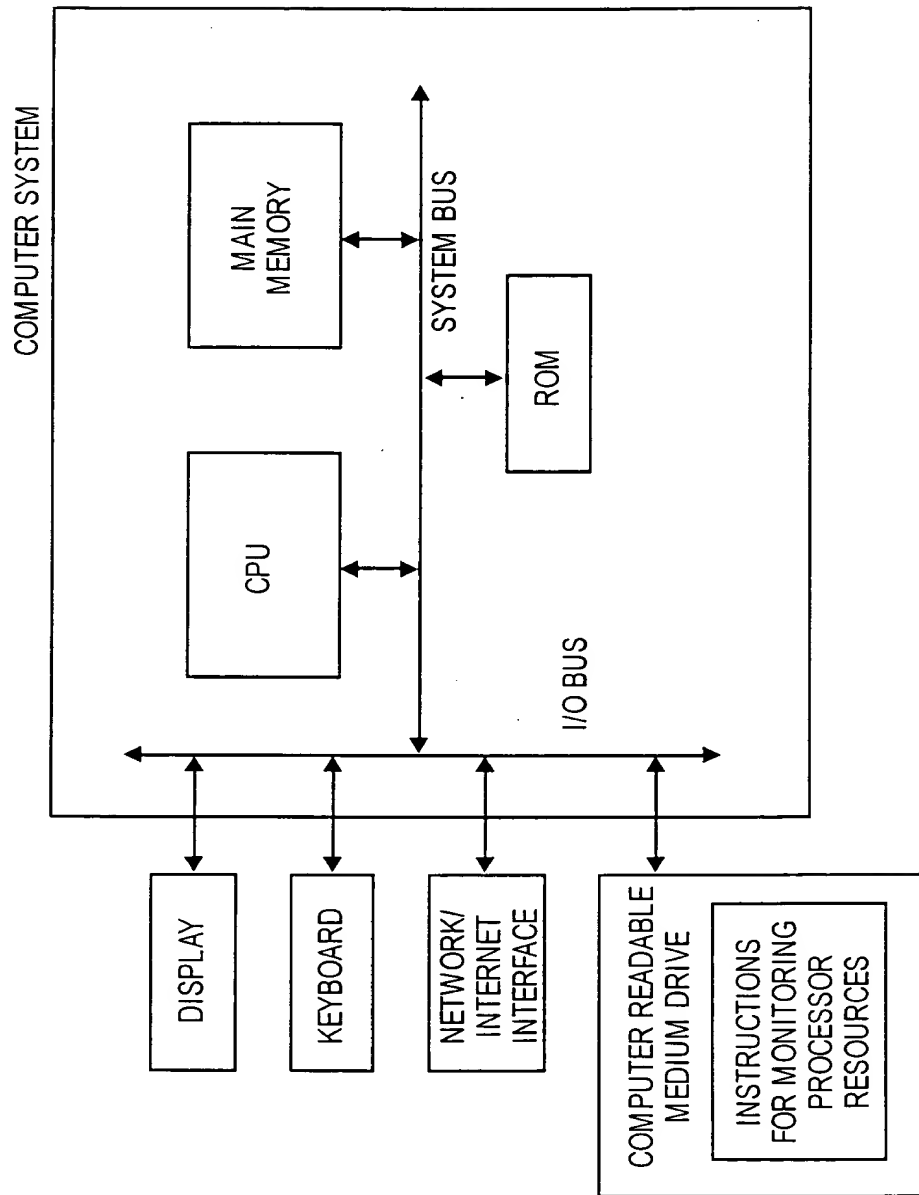


FIG. 3